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2133



PATENT  
Customer No. 22,852  
Attorney Docket No. 04329.2724

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
Masatsugu KITADA et al. ) Group Art Unit: 2133  
Application No.: 10/050,160 ) Examiner: James C. Kerveros  
Filed: January 18, 2002 )  
For: SEMICONDUCTOR TESTING ) Confirmation No.: 7613  
APPARATUS AND METHOD FOR )  
OPTIMIZING A WAIT TIME UNTIL )  
STABILIZATION OF )  
SEMICONDUCTOR DEVICE )  
OUTPUT SIGNAL )

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**PETITION FOR EXTENSION OF TIME**

Applicants hereby petition for a two month extension of time to reply to the Office Action of July 2, 2004. A fee of \$430.00 is enclosed.

Please grant any extensions of time required to enter this response and charge any additional required fees to our deposit account 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,  
GARRETT & DUNNER, L.L.P.

*Richard V. Burgujian*  
By: *Robert E. Converse, Jr. #27,432*  
Richard V. Burgujian  
Reg. No. 31,744

Dated: October 8, 2004  
10/12/2004 FPA/REIA 00000040 10050160  
01 FC:1252

430.00 DP